

产品型号	D358A-40V
产品信息	双运算放大器
产品简介	D358A 内部包含两个独立的，高增益，内部频率补偿的双运算放大器。适用于电压范围很宽的单电源，而且也适用于双电源工作方式。在推荐的工作条件下，与电源电压无关的低电源电压。
产品特点	<ul style="list-style-type: none"> <li>● 内部频率补偿</li> <li>● 大直流电压增益(约 100dB)</li> <li>● 单位增益频带宽(约 1MHz) (温度补偿)</li> <li>● 电源电压范围宽：单电源(3—40V)； 双电源(<math>\pm 1.5</math> — <math>\pm 15</math>V)</li> <li>● 低电源电流 —基本上是独立的电源电压 (1Nw/op amp 在 <math>\pm 5</math>VDC)</li> <li>● 温度补偿后低输入偏置电流：45 nADC</li> <li>● 低输入失调电压：2 m VDC, 低输入失调电流：50nA DC</li> <li>● 差分输入电压范围等于电源电压范围</li> <li>● 大输出电压振幅：0VDC to <math>V+ 1.5</math> VDC</li> </ul>
典型应用	
工作原理图	<p>The figure shows three circuit diagrams for the D358A-40V op-amp:</p> <ul style="list-style-type: none"> <li><b>Single Supply Inverting Amplifier:</b> The non-inverting input (+) is connected to ground. The inverting input (-) is connected to the input <math>v_{IN}</math> through a resistor <math>R_{IN}</math> and to the output <math>v_O</math> through a feedback resistor <math>R_F</math>. The output is also connected to a load resistor <math>R_L</math> to ground. The positive supply <math>V+</math> is connected to pin 8, and the negative supply is connected to pin 4, which is also connected to ground.</li> <li><b>Non-Inverting Amplifier:</b> The inverting input (-) is connected to ground. The non-inverting input (+) is connected to the input <math>v_{IN}</math>. The output <math>v_O</math> is connected to the inverting input (-) through a feedback resistor <math>R_F</math>. The positive supply <math>V+</math> is connected to pin 8, and the negative supply is connected to pin 4, which is also connected to ground.</li> <li><b>Input Biasing Voltage Follower:</b> The non-inverting input (+) is connected to the input <math>v_{IN}</math>. The inverting input (-) is connected to the output <math>v_O</math>. The positive supply <math>V+</math> is connected to pin 8 through a 10 k<math>\Omega</math> resistor. The negative supply is connected to pin 4 through a 10 k<math>\Omega</math> resistor. A resistor <math>R_1</math> is connected between the two supply pins. A feedback resistor <math>R_F</math> is connected between the output and the inverting input. The text "BLOCKS DC GAIN" is written near the input biasing network.</li> </ul>
通道	2
$V_{in}$ 范围	3 to 40 V
输入失调电压	7 mV
输入失调电流	150 nA
$I_{cc}$	0.6 mA
$I_{sink}$	10 mA
封装形式	SOP-8 / DIP-8
购买	联系客服服务中心